
SPH Simulations towards the exascale

Timothée David–Cléris*¹

¹CRAL UMR 5574 / ENS de LYON – Université de Lyon, ENS de Lyon – France

Résumé

We will present the features of SHAMROCK, a SPH code designed for massively parallel CPU/GPU architectures. A specific focus will be drawn on our novel tree algorithm optimised for modern GPUs & high core count CPUs. A significant speedup is achieved by using binary arithmetics & morton codes, allowing a nearly free recomputation of the tree.

*Intervenant